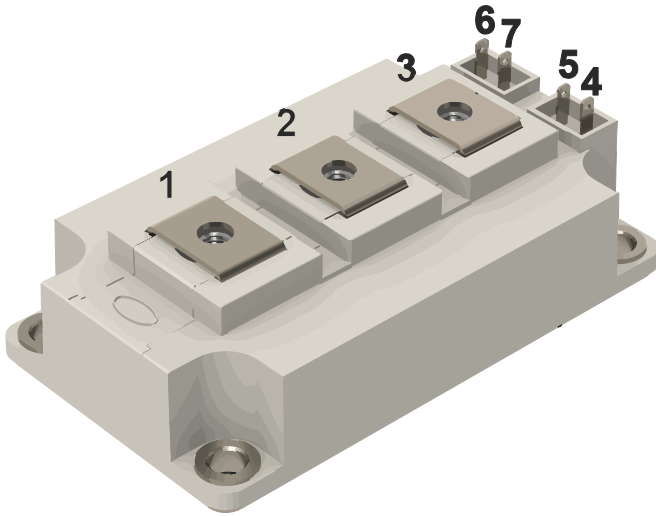


Industry standard 62mm IGBT module
1700V 150A

Chip features

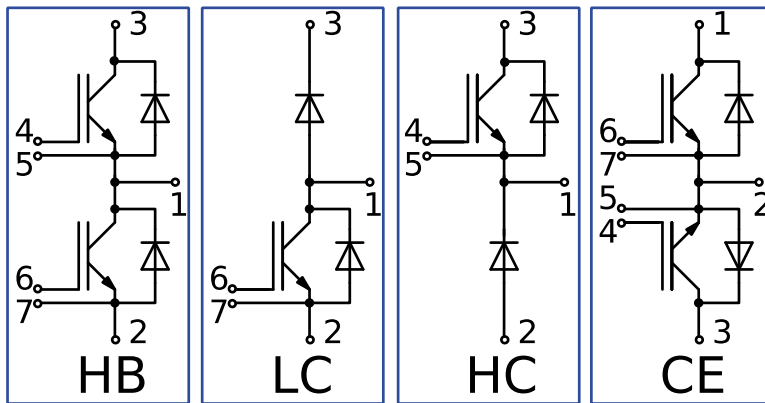
- IGBT Chip
 - low $V_{CE(sat)}$ value
 - 10 μs short circuit @ 150°C
 - square RBSOA @ $2 \times I_C$
 - low EMI
- FRD Chip
 - fast and soft reverse recovery
 - low voltage drop

Design features

- copper baseplate
- Al_2O_3 DBC substrate
- ultrasonically welded power terminals
- improved thermal cycling
- RoHS compliant

Typical application

- AC motor drives
- solar inverter
- air conditioning
- high power converters and UPS


Maximum rated values

Definition	Symbol	Conditions	Value	Unit
IGBT				
Collector-Emitter voltage	V_{CES}	$V_{GE} = 0V$	1700	V
Collector current (nominal)	$I_{C\ nom}$		150	A
Collector current (maximum continuous)	$I_{C\ 25}$	$T_j = 175^\circ C, T_c = 25^\circ C$	285	A
	$I_{C\ 80}$	$T_j = 175^\circ C, T_c = 80^\circ C$	215	A
Repetitive peak collector current *1	I_{CRM}	$I_{CRM} = 3 \times I_{C\ nom}, t_p = 1ms$	450	A
Gate-Emitter voltage	V_{GES}		± 20	V
Junction temperature	T_j		-40 ... 175	°C
Inverse diode / Freewheeling diode				
Repetitive peak reverse voltage	V_{RRM}	$V_{GE} = 0V$	1700	V
Forward current (nominal)	$I_{F\ nom}$		150	A
Forward current (maximum continuous)	$I_{F\ 25}$	$T_j = 175^\circ C, T_c = 25^\circ C$	204	A
	$I_{F\ 80}$	$T_j = 175^\circ C, T_c = 80^\circ C$	153	A
Repetitive peak forward current *1	I_{FRM}	$I_{FRM} = 2 \times I_{F\ nom}, t_p = 1ms$	300	A
Surge (non-repetitive) forward current	I_{FSM}	$t_p = 1ms, \sin 180^\circ, T_j = 25^\circ C$	950	A
Junction temperature	T_j		-40 ... 175	°C
Module				
Storage temperature	T_{stg}		-40 ... 125	°C
Isolation voltage	V_{isol}	AC sinus 50 Hz, $t = 1min$	4000	V

*1 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed maximum T_j rating.



Characteristics

Definition	Symbol	Conditions	Value			Unit	
			min.	typ.	max.		
IGBT							
Collector-Emitter saturation voltage	V_{CEsat} (chip)	$V_{GE} = 15V$ $I_C = 150A$	$T_J = 25^\circ C$	-	1.90	2.20	V
			$T_J = 150^\circ C$	-	2.25	2.45	V
	V_{CEsat} (terminal)		$T_J = 25^\circ C$	-	2.00	2.30	V
			$T_J = 150^\circ C$	-	2.35	2.55	V
Gate-Emitter threshold voltage	$V_{GE(th)}$	$I_C = 6mA, V_{CE} = V_{GE}, T_J = 25^\circ C$	5.2	5.8	6.4	V	
Collector-Emitter cut-off current	I_{CES}	$V_{CE} = 1700V$ $V_{GE} = 0V$	$T_J = 25^\circ C$	-	-	2000	μA
			$T_J = 150^\circ C$	-	-	3000	μA
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	600	nA	
Input capacitance	C_{ies}	$V_{CE} = 25V, V_{GE} = 0V$ $f = 1MHz, T_J = 25^\circ C$	-	12000	-	pF	
Output capacitance	C_{oes}		-	500	-	pF	
Reverse transfer capacitance	C_{res}		-	380	-	pF	
Total gate charge	Q_G	$I_C = 150A, V_{CE} = 600V, V_{GE} = -8 \div 20V$	-	1200	-	nC	
Internal gate resistance	R_{Gint}	$T_J = 25^\circ C$	3.7	5.0	6.3	Ω	
Turn-on delay time	$t_{d(on)}$	$V_{CE} = 1200V, I_C = 150A,$ $R_G = 2.0\Omega, V_{GE} = \pm 15V,$ $T_J = 150^\circ C,$ $di/dt_{on} = 3500 A/\mu S,$ $di/dt_{off} = 890 A/\mu S,$ $du/dt_{off} = 5440 V/\mu S$	-	234	281	ns	
Rise time	t_r		-	41	49	ns	
Turn-on energy	E_{on}		-	67	80	mJ	
Turn-off delay time	$t_{d(off)}$		-	671	805	ns	
Fall time	t_f		-	144	173	ns	
Turn-off energy	E_{off}		-	59	71	mJ	
Short circuit duration	t_{psc}	$V_{CC} = 720V, V_{GE} \leq 20V,$ $T_J = 150^\circ C, \text{non-repetitive}$	-	-	10	μs	
Collector-emitter threshold voltage	V_{CE0}	$V_{GE} = 15V, T_J = 150^\circ C$	-	1	1.1	V	
On-State slope resistance (IGBT)	r_{CE0}	for static power loss calculation	-	8.33	9.0	m Ω	
Thermal resistance junction to case	$R_{th(j-c)}$	per IGBT	-	-	0.135	K/W	
Inverse diode / Freewheeling diode							
Continuous forward voltage	V_F (chip)	$I_F = 150A$	$T_J = 25^\circ C$	-	1.98	2.37	V
			$T_J = 150^\circ C$	-	2.1	2.5	V
	V_F (terminal)		$T_J = 25^\circ C$	-	2.1	2.47	V
			$T_J = 150^\circ C$	-	2.2	2.6	V
Reverse recovery time	t_{rr}	$V_{CC} = 1200V$ $I_F = 150A$ $di_F/dt = 2410 A/\mu S$	$T_J = 25^\circ C$	-	-	-	ns
Peak reverse recovery current	I_{RM}		$T_J = 125^\circ C$	-	-	-	ns
			$T_J = 25^\circ C$	-	180	216	A
Reverse recovered charge	Q_{rr}		$T_J = 150^\circ C$	-	240	288	A
			$T_J = 25^\circ C$	-	45	54	μC
Reverse recovery energy	E_{rr}		$T_J = 150^\circ C$	-	85	102	μC
		$T_J = 25^\circ C$	-	20	24	mJ	
Forward threshold voltage (Diode)	V_{F0}	$T_J = 150^\circ C$	-	1.08	1.22	V	
On-state slope resistance (Diode)	r_F	for static power loss calculation	-	6.9	8.7	m Ω	
Thermal resistance junction to case	$R_{th(j-c)}$		-	-	0.25	K/W	
Module							
Parasitic inductance Collector-Emitter	L_{CE}		-	30	-	nH	
Resistance terminal-chip	$R_{CC'+REE'}$	terminal-chip $T_J = 25^\circ C$	-	0.5	-	m Ω	
Thermal resistance case to heatsink	R_{thCH}	per module	-	0.05	-	K/W	
Mounting torque for screws to heatsink	M_s	to heatsink M6	3	-	5	Nm	
Mounting torque for terminal screws	M_t	to terminals M5	2.5	-	5	Nm	
Weight	W		-	-	340	g	

