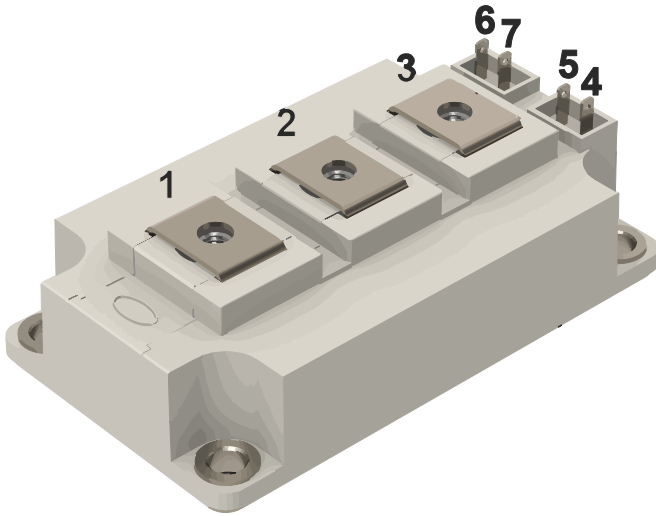


Industry standard 62mm IGBT module
1700V 200A

Chip features

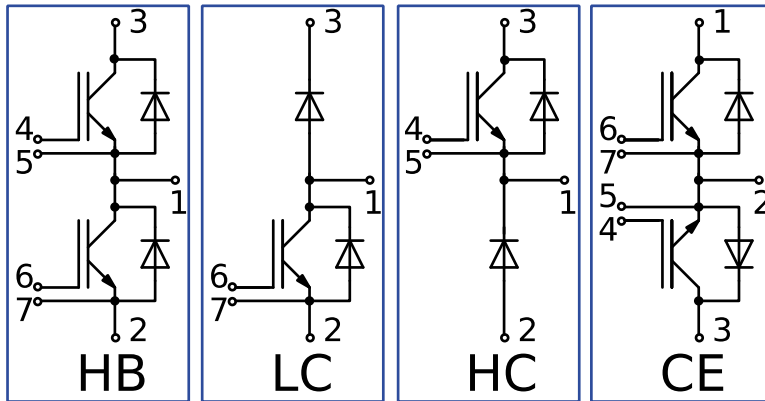
- IGBT Chip
 - low $V_{CE(sat)}$ value
 - 10 μs short circuit @ 150°C
 - square RBSOA @ $2 \times I_C$
 - low EMI
- FRD Chip
 - fast and soft reverse recovery
 - low voltage drop

Design features

- copper baseplate
- Al₂O₃ DBC substrate
- ultrasonically welded power terminals
- improved thermal cycling
- RoHS compliant

Typical application

- AC motor drives
- solar inverter
- air conditioning
- high power converters and UPS


Maximum rated values

Definition	Symbol	Conditions	Value	Unit
IGBT				
Collector-Emitter voltage	V_{CES}	$V_{GE} = 0V$	1700	V
Collector current (nominal)	$I_{C\ nom}$		200	A
Collector current (maximum continuous)	$I_{C\ 25}$	$T_j = 175^\circ C, T_c = 25^\circ C$	325	A
	$I_{C\ 80}$	$T_j = 175^\circ C, T_c = 80^\circ C$	250	A
Repetitive peak collector current *1	I_{CRM}	$I_{CRM} = 3 \times I_{C\ nom}, t_p = 1ms$	600	A
Gate-Emitter voltage	V_{GES}		± 20	V
Junction temperature	T_j		-40 ... 175	°C
Inverse diode / Freewheeling diode				
Repetitive peak reverse voltage	V_{RRM}	$V_{GE} = 0V$	1700	V
Forward current (nominal)	$I_{F\ nom}$		200	A
Forward current (maximum continuous)	$I_{F\ 25}$	$T_j = 175^\circ C, T_c = 25^\circ C$	205	A
	$I_{F\ 80}$	$T_j = 175^\circ C, T_c = 80^\circ C$	151	A
Repetitive peak forward current *1	I_{FRM}	$I_{FRM} = 2 \times I_{F\ nom}, t_p = 1ms$	400	A
Surge (non-repetitive) forward current	I_{FSM}	$t_p = 1ms, \sin 180^\circ, T_j = 25^\circ C$	1170	A
Junction temperature	T_j		-40 ... 175	°C
Module				
Storage temperature	T_{stg}		-40 ... 125	°C
Isolation voltage	V_{isol}	AC sinus 50 Hz, t = 1min	4000	V

*1 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed maximum T_j rating.



Characteristics

Definition	Symbol	Conditions	Value			Unit	
			min.	typ.	max.		
IGBT							
Collector-Emitter saturation voltage	V_{CEsat} (chip)	$V_{GE} = 15V$ $I_C = 200A$	$T_J = 25^\circ C$	-	1.90	2.20	V
			$T_J = 150^\circ C$	-	2.30	2.60	V
	V_{CEsat} (terminal)		$T_J = 25^\circ C$	-	2.00	2.30	V
			$T_J = 150^\circ C$	-	2.40	2.70	V
Gate-Emitter threshold voltage	$V_{GE(th)}$	$I_C = 8mA, V_{CE} = V_{GE}, T_J = 25^\circ C$	5.2	5.8	6.4	V	
Collector-Emitter cut-off current	I_{CES}	$V_{CE} = 1700V$ $V_{GE} = 0V$	$T_J = 25^\circ C$	-	-	2700	μA
			$T_J = 150^\circ C$	-	-	3500	μA
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	1200	nA	
Input capacitance	C_{ies}	$V_{CE} = 25V, V_{GE} = 0V$ $f = 1MHz, T_J = 25^\circ C$	-	18000	-	pF	
Output capacitance	C_{oes}		-	680	-	pF	
Reverse transfer capacitance	C_{res}		-	580	-	pF	
Total gate charge	Q_G	$I_C = 150A, V_{CE} = 600V, V_{GE} = -8 \div 20V$	-	1600	-	nC	
Internal gate resistance	R_{Gint}	$T_J = 25^\circ C$	-	3.75	-	Ω	
Turn-on delay time	$t_{d(on)}$	$V_{CE} = 1200V, I_C = 200A,$ $R_G = 2.0\Omega, V_{GE} = \pm 15V,$ $T_J = 150^\circ C,$ $di/dt_{on} = 6830 A/\mu S,$ $di/dt_{off} = 1120 A/\mu S,$ $du/dt_{off} = 5250 V/\mu S$	-	258	310	ns	
Rise time	t_r		-	35	42	ns	
Turn-on energy	E_{on}		-	69	83	mJ	
Turn-off delay time	$t_{d(off)}$		-	712	854	ns	
Fall time	t_f		-	149	179	ns	
Turn-off energy	E_{off}		-	79	95	mJ	
Short circuit duration	t_{psc}	$V_{cc} = 720V, V_{GE} \leq 20V,$ $T_J = 150^\circ C, \text{non-repetitive}$	-	-	10	μs	
Collector-emitter threshold voltage	V_{CE0}	$V_{GE} = 15V, T_J = 150^\circ C$	-	0.7	0.8	V	
On-State slope resistance (IGBT)	r_{CE0}	for static power loss calculation	-	8.00	9.0	m Ω	
Thermal resistance junction to case	$R_{th(j-c)}$	per IGBT	-	-	0.123	K/W	
Inverse diode / Freewheeling diode							
Continuous forward voltage	V_F (chip)	$I_F = 200A$	$T_J = 25^\circ C$	-	2.00	2.40	V
			$T_J = 150^\circ C$	-	2.15	2.57	V
	V_F (terminal)		$T_J = 25^\circ C$	-	2.10	2.5	V
			$T_J = 150^\circ C$	-	2.25	2.67	V
Peak reverse recovery current	I_{RM}	$V_{cc} = 1200V$ $I_F = 200A$ $di_F/dt = 3200 A/\mu S$	$T_J = 25^\circ C$	-	200	240	A
Reverse recovered charge	Q_{rr}		$T_J = 150^\circ C$	-	260	312	A
			$T_J = 25^\circ C$	-	50	60	μC
Reverse recovery energy	E_{rr}		$T_J = 150^\circ C$	-	90	108	μC
			$T_J = 25^\circ C$	-	23	27.6	mJ
$T_J = 150^\circ C$	-		50	60	mJ		
Forward threshold voltage (Diode)	V_{F0}	$T_J = 150^\circ C$	-	1.08	1.22	V	
On-state slope resistance (Diode)	r_F	for static power loss calculation	-	5.4	6.8	m Ω	
Thermal resistance junction to case	$R_{th(j-c)}$		-	-	0.28	K/W	
Module							
Parasitic inductance Collector-Emitter	L_{CE}		-	30	-	nH	
Resistance terminal-chip	$R_{CC'}+R_{EE'}$	terminal-chip $T_J = 25^\circ C$	-	0.5	-	m Ω	
Thermal resistance case to heatsink	R_{thCH}	per module	-	0.05	-	K/W	
Mounting torque for screws to heatsink	M_s	to heatsink M6	3	-	5	Nm	
Mounting torque for terminal screws	M_t	to terminals M5	2.5	-	5	Nm	
Weight	W		-	-	340	g	

